**Publication**

1.A Review of an Intelligent System for Detecetion of Potholes and Humps By:Mohod, SK (Mohod, Swati K.)[1 ]; Bhoyar, DB (Bhoyar, Dinesh B.)[2 ]; Khade, SS (Khade, S. S.)[2 ]; Kulat, KD (Kulat,K. D.)[3] Web of Science [v.5.32] - Web of Science Core Collection Full Record

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2.Design and Implementation of AES Data Security Protocol  
 By:Bhoyar, DB (Bhoyar, Dinesh B.)[ 1 ] ; Kaur, I (Kaur, Indersheel)[ 1 ]

; Mohod, SK (Mohod, Swati K.)[ 2 ] ; Kulat, KD (KulatK. D.)[3]

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3.Ph and NPK Sensor Based Soil Testing System By:Bhoyar, DB

(Bhoyar, Dinesh B.)[ 1 ] ; Kaur, I (Kaur, Indersheel)[ 1 ]

; Mohod, SK (Mohod, Swati K.)[ 2 ] ; Kulat, KD (KulatK. D.)[3]

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4.B- ALERT PATROL SPONTANEOUS DETECETION OF POTHOLES

AND HUMPS Anjula Wararkar1,Swati K. Mohod2 Swaranjali

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5.Channel Estimation technique for high speed wireless

communication in noisy environment” International Journal of

Future generation Communication and Networking Vol 13 No-1 (2020)

pp 1106-1112

6.Design of soil nutrient system for Indian crops” International

Journal of Future generation Communication and Networking Vol

No-1 (2020) pp 1446-1452

7. International Conference on Innovation &research in engineering, science & technology Tulsiramji Gaikwad-Patil College of Engineering & tech ,Nagpur

8. Design and Implementation of Orange Sorting Process””

International Journal of Future generation Communication and Networking

9. IEEE International conference on telecommunication & computing Design of

majority function one bit adder circuit for low power vlsi application18-20

December 2013 at Kavikulguru Institute of tech & science,Ramtek

10. MGI PG Conference 2014 under the activity of R & D – Quality of

Publication National Design of Combinational Logic ckt for low power VLSI

application 09 may 2014.