

Research Paper Publication Details

Sr. No.	Name of Faculty	Co-Author	Paper name	Event	Name of Journal / Conference	Organised/ Sponsored by	Date	Status
				Journal / Conference				National / International
1	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur, Mr. Akshay Kadu, YCCE, Nagpur	New Improved ACO Algorithm for Selective Harmonic Elimination in Multilevel Inverter	Journal	Turkish Journal of Computer and Mathematics Education (TURCOMAT) (Scopus Indexed)	Vol.12 No. 11 (2021), 2969-2977	10.Mai.21	International
2	Sarika D. Patil	Mr. Akshay Kadu, YCCE, Nagpur, Mr. Pratik Dhabe, SSGMCE, Shegaon (M.S.)	Improved Control Strategy for Harmonic Mitigation in Multilevel Inverter	Conference	2021 5th International Conference on Intelligent Computing and Control Systems (ICICCS)	IEEE Vaigai College of Engineering, Madurai	6-8 May 2021	International
3	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Hybrid Optimization Approach Applied for Harmonic Reduction in Multilevel Inverter	Conference	First IEEE International Conference on Power, Control and Computing Technologies	ICPC2T, NIT, Raipur	1-Jan-20	International
4	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Application of Ant Colony Optimization Technique for Harmonic Elimination in Grid Tied Inverter	Conference	International Conference on Electronics communication, Robotics, Data Mining, Information Science and Electrical Engg	Jawaharlal Nehru University (JNU), New Delhi	19-Mar-20	International
5	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Hybrid optimization algorithm applied for harmonic elimination for multilevel inverter with reduced switch topology	Journal	Micro System Technologies, Vol.24, Issue 2, pp 1-7, (Springer) - SCI Indexed		1-Dec-19	International
6	Sarika D. Patil	Dr. S.G. Kadwane, Professor, YCCE Nagpur, Dr. S.P. Gawande, YCCE, Nagpur	Current Control of Grid Tied Inverter through SHEPWM Method	Journal	Elsevier- Energy Procedia, Vol.117, pp 643-650 (Scopus Indexed)		01.Jun.17	International
7	Sarika D. Patil		Speed Control of Single Phase Induction Motor Using Single MOSFET	Conference	2018 International Conference on Smart Electric Drives and Power System	IEEE , Sponsored (ICSEDPS), Nagpur	18.Nov.18	International
8	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Application of optimisation technique in SHE controlled multilevel inverter	Conference	2017 International Conference on Energy, Communication, Data Analytics and Soft Computing	IEEE , Sponsored ICECDS, Chennai	1-2 Aug 2017	International
9	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Ant colony optimization applied to selective harmonic elimination in multilevel inverter	Conference	2 nd International Conference on Applied and Theoretical Computing and Communication Technology (iCATccT-2016)	IEEE , Sponsored iCATccT 2016, Bengaluru	21-23 July 2016	International
10	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	THD Minimization of Three phase voltage in fivelevel cascaded H bridge inverter	Journal	Journal of Electrical and Electroincs Engineering, Vol.11, issue2, April 2016.	(IOSR) Journal	01.Apr.16	International
11	Sarika D. Patil		Comparision of PWM technique and selective harmonic elimination for three level inverter	Journal	International journal of engineering research and application, Vol.6, issue 4, April 2016.	(IJERA) Journal	01.Apr.16	International
12	Sarika D. Patil		Implementation of Firing Scheme for Multilevel Inverter and its Hardware Implementation	Journal	International Journal of Recent and Innovation Trends in Computing and Communication	IJRITCC, Vol. 4, Issue 5.	16.Mai.16	International
13	Sarika D. Patil		Analysis for selective harmonic Elimination in a multilevel inverter	Conference	National Conference on Green power and Energy: A step towards better future	GHRCE,Nagpur	16.Jän.20	National
14	Sarika D. Patil		Discrete Space Vector Modulation Technique(DSVM) for Direct Torque Control of Induction Motor	Conference	National conference on advancese in Engineering, Technology and Applied Sciences	YCCE, Nagpur	15.Mär.15	National

15	Sarika D. Patil		Selective Harmonic Reduction Technique for a Multilevel Inverter using optimization approach	Conference	6th National Conference on Multidisciplinary Research in Science & Engineering,	MIT COE Alandi, Pune	June-15	National
16	Sarika D. Patil	Dr. S.G. Kadwane, professor, YCCE Nagpur	Selective Harmonic Elimination With Reduced Switch Topology in Multilevel Inverter	Journal	International Journal of Science, Technology & Management	IJSTM, Vol.04, Special Issue No. 01	15.Mär.15	International
17	Sarika D. Patil		Voltage Improvement in Grid connected system by FACTS devices	Conference	National Conference on Emerging Technology Trends in Engineering	KDKCE	11.Feb.08	National
18	Sarika D. Patil		Stabilization of Power System with Wind Connected Grid by STATCOM	Conference	International conference CO-GEN-2008	Institution Of Engineers, Nagpur Local Centre, Nagpur IE(I)	Feb.08	International

